

Product Summary

V_{DS} (V)	$R_{DS(on),max}$ (mΩ)	I_D (A)
150	60 @ $V_{GS} = 10V$	28 ⁽¹⁾

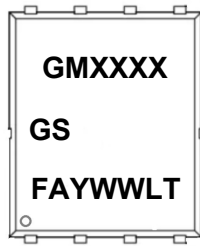
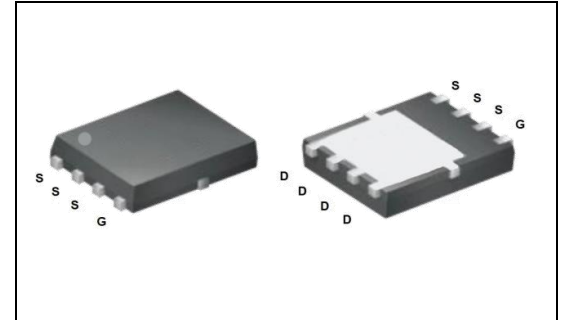
Features

- Low $R_{DS(on)}$ SGT technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Application

- Primary side switch
- Synchronous rectification
- DC/DC conversion
- Motor controls

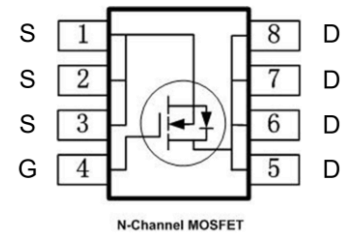
PDFN5060



PDFN5060

NOTE:
 LOGO - GS
 GMXXXX- Part number code
 F - Fab location code
 A - Assembly location code
 Y - Year code
 WW - Week code
 L&T - Assembly lot code

Equivalent circuit



Absolute maximum rating@25°C

PARAMETER	SYMBOL	LIM	UNIT
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	$T_C = 25\text{ °C}$	I_D 28	A
	$T_C = 70\text{ °C}$	17.2	
Pulsed Drain Current ($t = 300\text{ }\mu\text{s}$)	I_{DM}	90	A
Single Pulse Avalanche Energy	$L = 1.0\text{ mH}$	E_{AS} 70	mJ
Maximum Power Dissipation	$T_C = 25\text{ °C}$	P_D 62.5	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	°C
Soldering Recommendations (Peak Temperature) ^{c,d}		260	

Thermal Characteristic

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient	$t \leq 10\text{ s}$	R_{thJA} 26	33	°C/W
Maximum Junction-to-Case	Steady State	R_{thJC} 1.6	2.0	

Electrical Characteristics (T_J=25 °C unless otherwise noted)

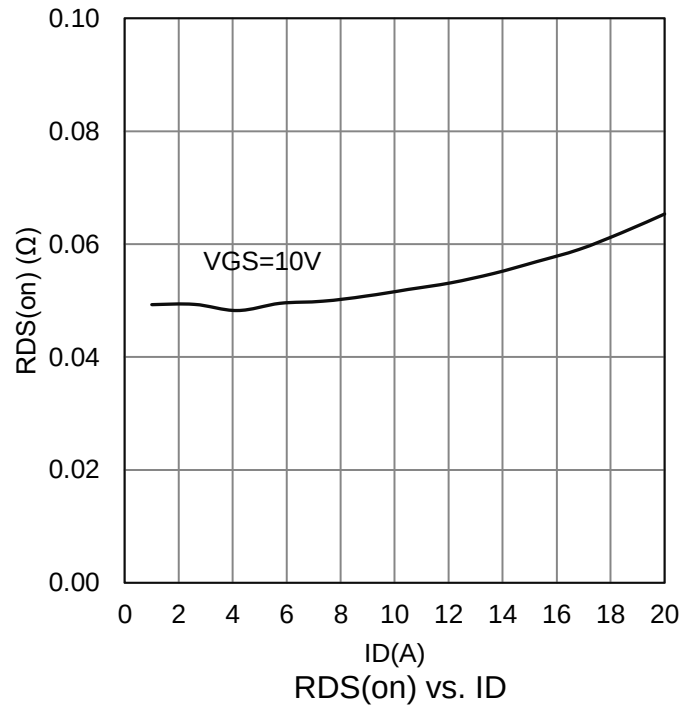
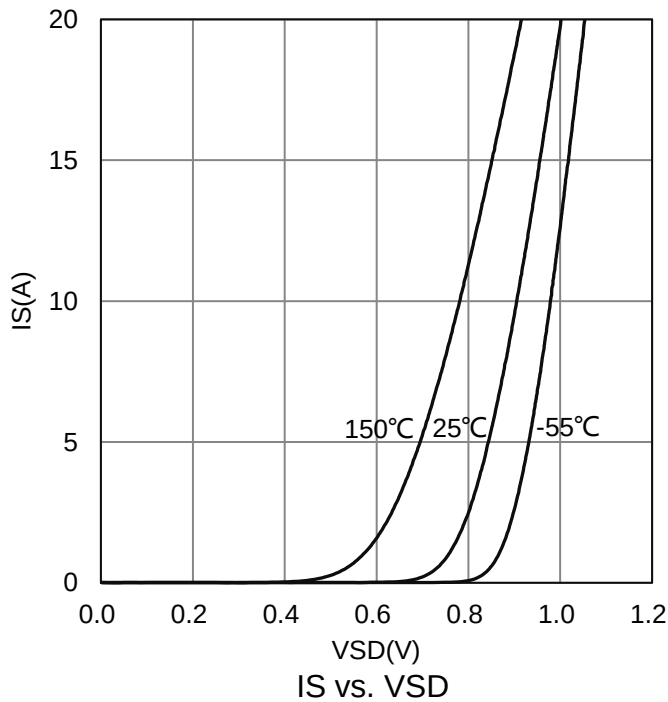
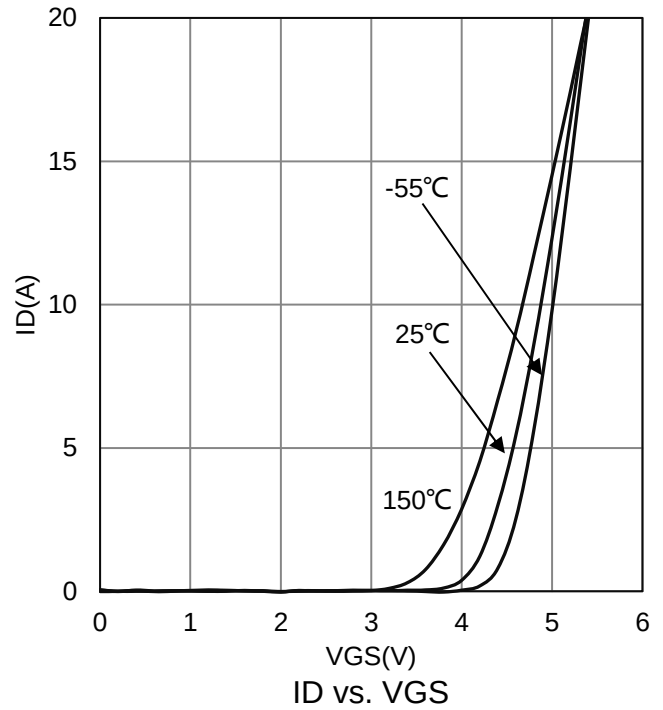
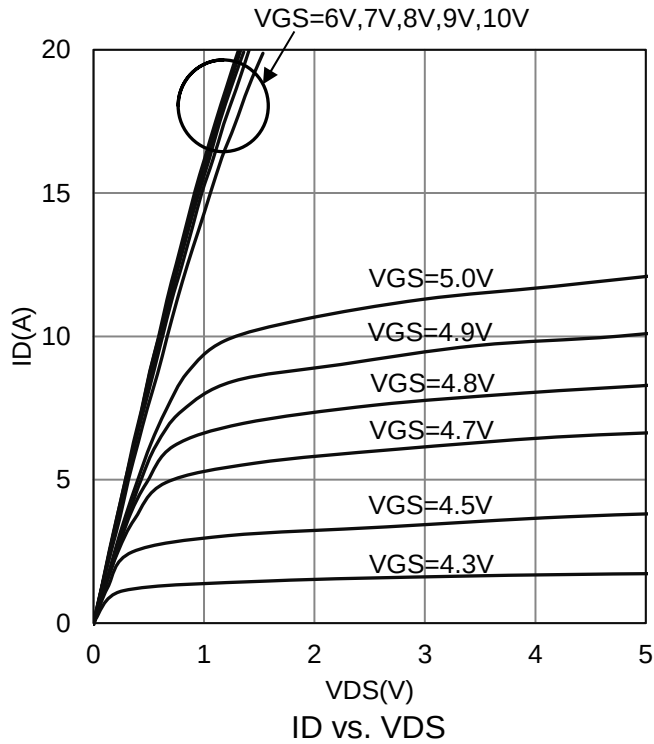
PARAMETER	S MBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	150			V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.5	3.4	4.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 150 V, V _{GS} = 0 V			1	μA
		V _{DS} = 150 V, V _{GS} = 0 V, T _J = 55 °C			10	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 10 A		50	60	mΩ
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 10 A		18		S
Dynamic ^b						
Input Capacitance	C _{iSS}	V _{DS} = 75 V, V _{GS} = 0 V, f = 1 MHz		732		pF
Output Capacitance	C _{oSS}			65		
Reverse Transfer Capacitance	C _{rSS}			7.2		
Total Gate Charge	Q _g	V _{DS} = 75 V, V _{GS} = 10 V, I _D = 10 A		6.9		nC
Gate-Source Charge	Q _{gs}	V _{DS} = 75 V, V _{GS} = 7.5 V, I _D = 10 A		5.1		
Gate-Drain Charge	Q _{gd}			2.3		
Gate Resistance	R _g			1.9		
Gate Resistance	R _g	f = 1 MHz		0.9		Ω
Turn-On Delay Time	t _{d(on)}	V _{DS} = 75 V, I _D ≅ 10 A, V _{GS} = 10 V, R _g = 3 Ω		4.0		ns
Rise Time	t _r			3.0		
Turn-Off Delay Time	t _{d(off)}			5.9		
Fall Time	t _f			2.1		
Drain-Source Body Diode Characteristics						
Body Diode Voltage	V _{SD}	I _S = 4 A, V _{GS} = 0 V		0.7	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, dI/dt = 100 A/μs, T _J = 25 °C		58		ns
Body Diode Reverse Recovery Charge	Q _{rr}			120		nC

Notes

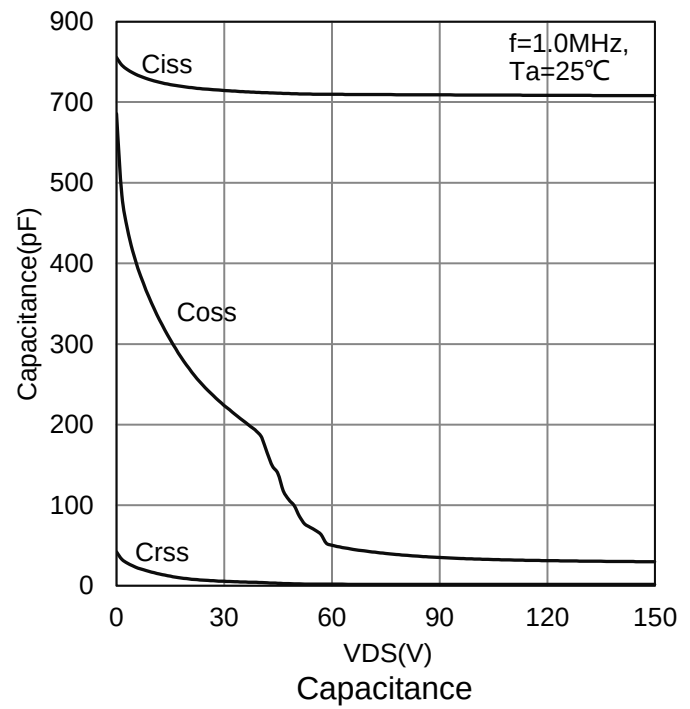
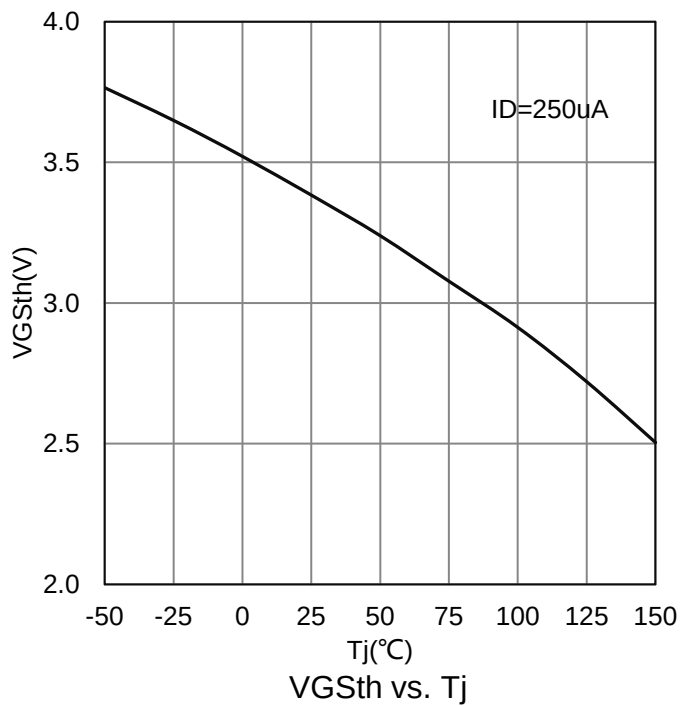
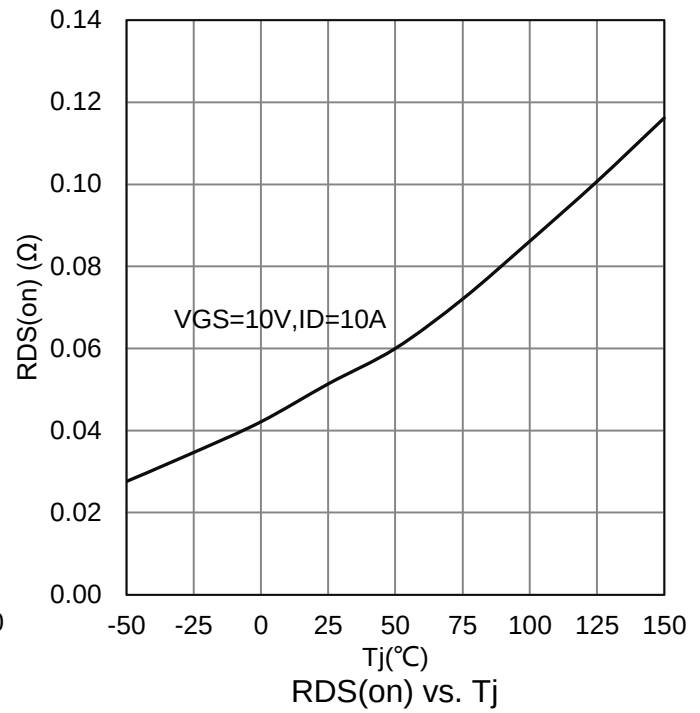
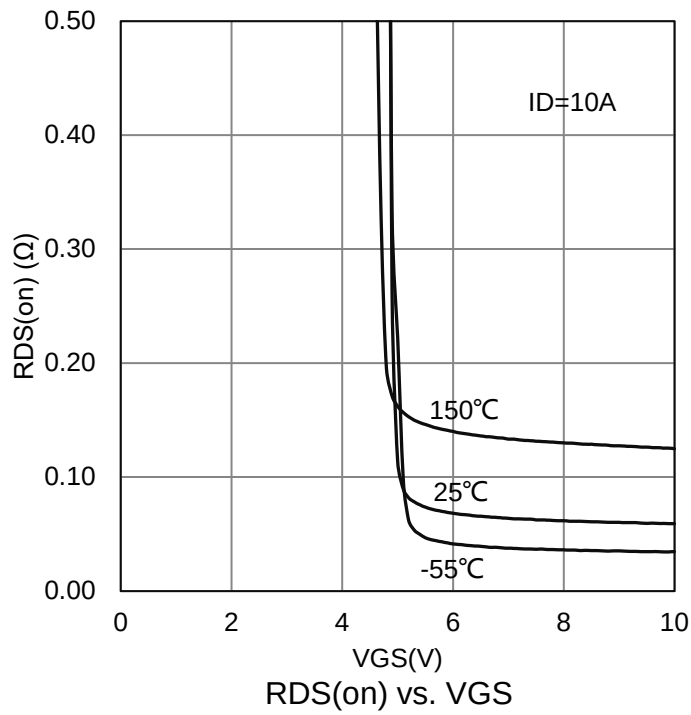
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.



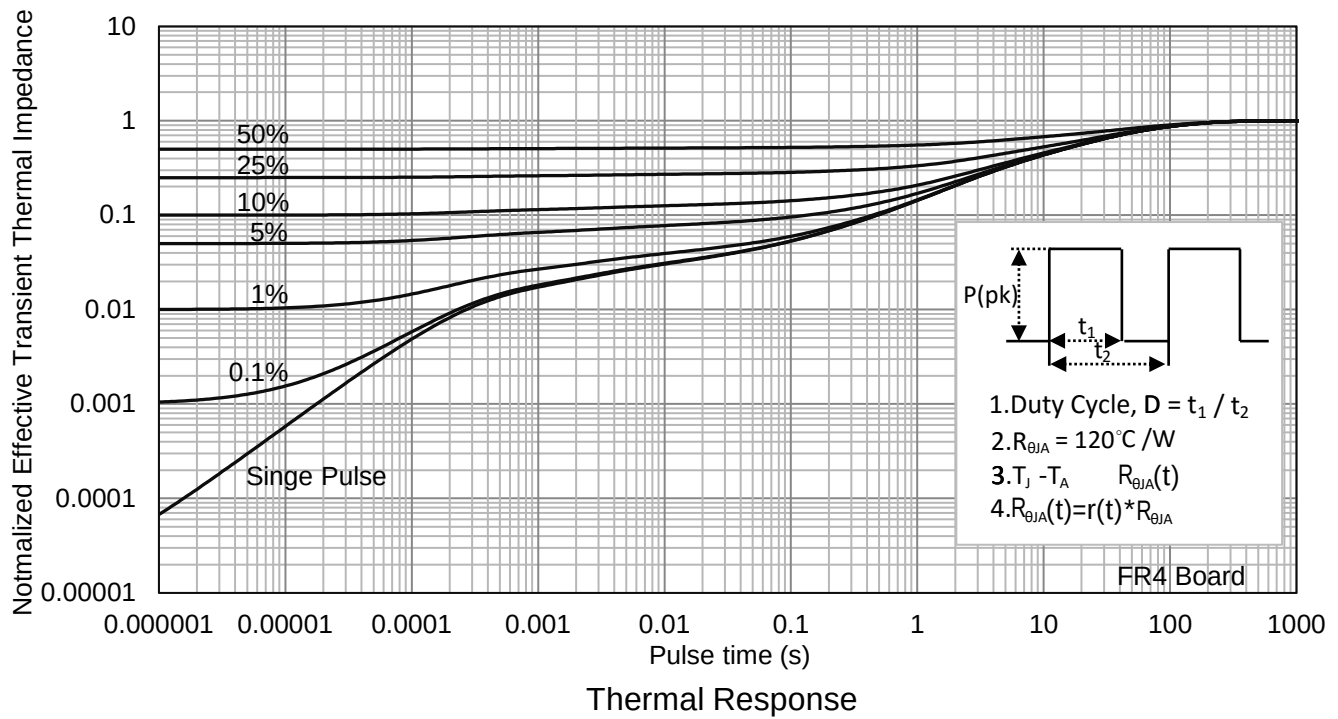
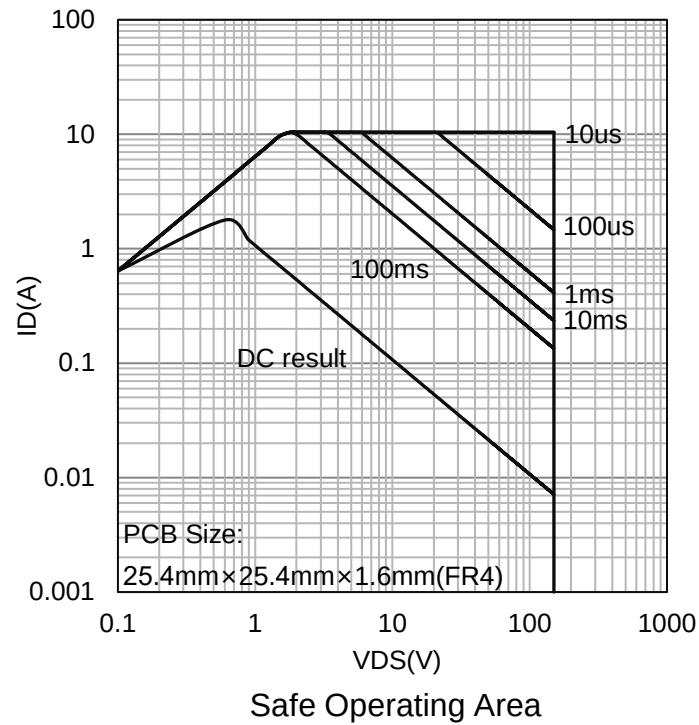
Typical Performance Characteristics



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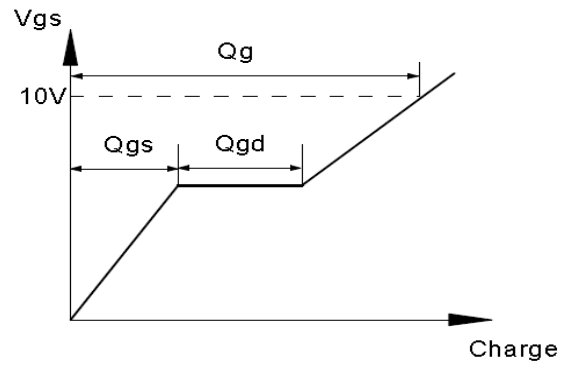
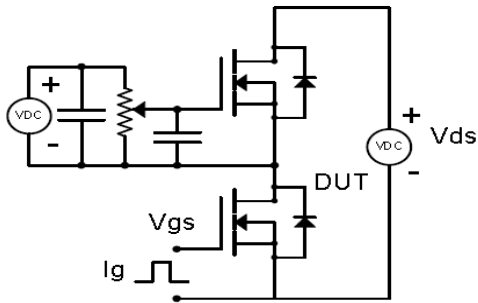


Typical Performance Characteristics

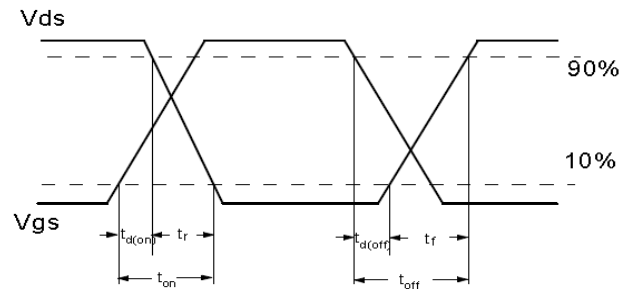
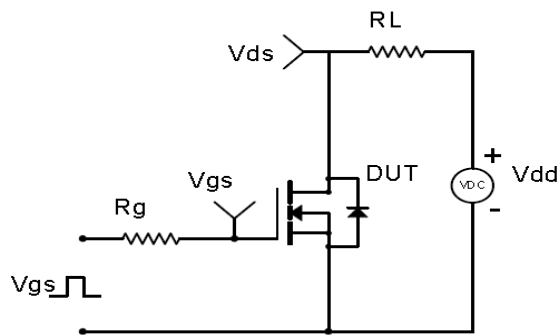


Test Circuit & Waveform

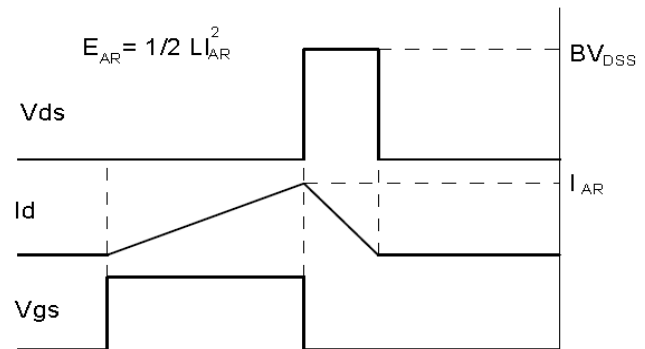
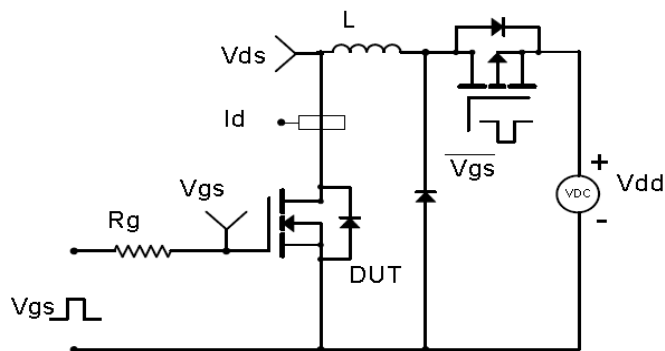
Gate Charge Test Circuit & Waveform



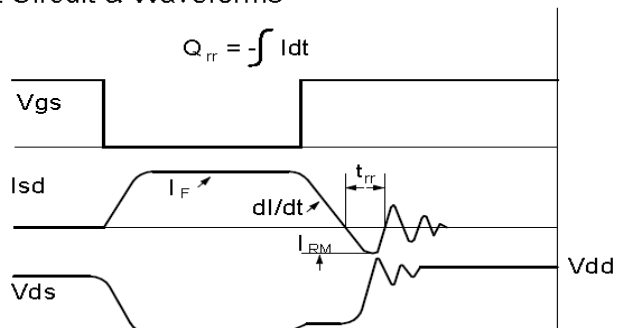
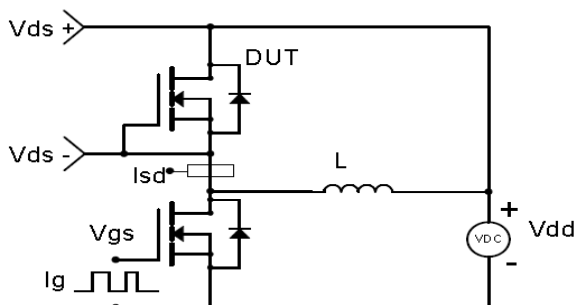
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

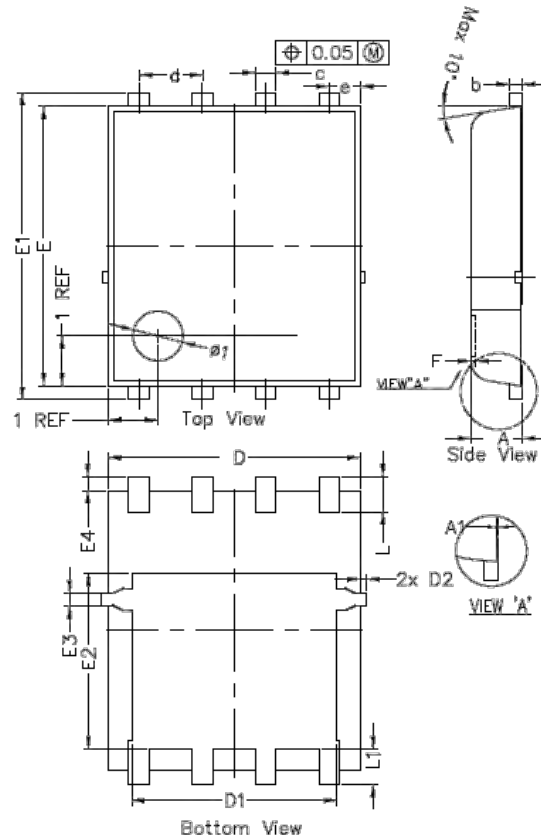


Diode Recovery Test Circuit & Waveforms





Outline Drawing PDFN5060



Dim	Millimeters		
	Min	Nom	Max
A	0.900	1.000	1.100
A1	0.000	---	0.050
b	0.246	0.254	0.312
c	0.310	0.410	0.510
d	1.27BSC		
D	4.950	5.050	5.150
D1	4.000	4.100	4.200
D2	---	---	0.125
e	0.62BSC		
E	5.500	5.600	5.700
E1	6.050	6.150	6.250
E2	3.425	3.525	3.625
E3	0.150	0.250	0.350
E4	0.175	0.275	0.375
F	---	---	0.100
L	0.500	0.600	0.700
L1	0.600	0.700	0.800

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